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TTL/CMOS logic chips comparison table for the "Retro Chip Tester Professional"

Tests that have been verified with a real chip are highlighted in green.

Tests that have been reported to work but have not been verified yet are highlighted in yellow.

Tests marked in red have never been tested with a real chip but may work.

"RAM" or "ROM": use the corresponding entry in the main menu; "n/a": cannot be implemented; "TBD": to be done

Note: ICs that require an input current of >3mA may not be able to be tested with 1k Ohm protection (most likely bipolar ICs without any LS, H, L, HC, etc).

Please refer to the manual when ICs with a higher current requirement should be tested. 470 Ohm protection might be required.

Some of these ICs are marked with "(!)". Please refer the manual before testing this chip. "(*)" means analog device, test result has limited informative value.

If you have some of the chips marked in yellow/red or not listed and do not need them, I would appreciate these to complete the tests.

Notes:

Fairchild 9xXXX = 74xXXX (e.g. 9LS54 = 74LS65), 93XXX = 74XXX (e.g. 93170 = 74170) test with 74XXX, CMOS: 14xxx = 40xxx (e.g. HD14193 = 40193) test with 40xxx

Identifier	Pins	Status	UdSSR	UdSSR	DDR	Cyrillic	Philips	Siemens	Remark
7400	14	v15	KR1531LA3	K555LA3	DL000D	ЛА3	FJH131	FLH101	Quad 2-Input NAND Gate
7401	14	v14		K555LA8		ЛА8	FJH231	FLH201	Quad 2-Input NAND Gate with Open Collector Outputs
7402	14	v15	KR531LE1	K555LE1	DL002N	ЛЕ1	FJH221	FLH191	Quad 2-Input NOR Gate
7403	14	v14		K555LA9	DL003D	ЛА9	FJH291	FLH291	Quad 2-Input NAND Gate with Open Collector Outputs (different pinout than 7401)
7404	14	v15	KR1531LN1	K555LN1	DL004D	ЛН1	FJH241	FLH211	Hex Inverter
7405	14	v15	KR531LN2	K555LN2		ЛН2	FJH251	FLH271	Hex Inverter
7406	14	v14		K555LN3		ЛН3		FLH481	Hex Inverter
7407	14	v14		K555LP9		ЛП9		FLH491	Hex Buffer/Driver with 30V Open collector Outputs
7408	14	v15	KR531L11	K555L11	DL008D	ЛМ1		FLH381	Quad 2-Input AND Gate
7409	14	v13		K555L12		ЛМ2		FLH391	Quad 2-Input AND Gate with Open collector Outputs
7410	14	v15	KR1531LA4	K555LA4	DL101D	ЛА4	FJH121	FLH111	Triple 3-Input NAND Gate
7411	14	v14	KR531L13	K555L13	DL011D	ЛМ3		FLH581	Triple 3-Input AND Gate
7412	14	v13	K155LA10	K555LA10		ЛА10		FLH501	Triple 3-Input NAND Gate with Open Collector Outputs
7413	14	v15	K155TL1			ТЛ1	FJL131	FLH351	Dual Schmitt trigger 4-Input NAND Gate
7414	14	v14	K155TL2	K555TL2	DL014D	ТЛ2			Hex Schmitt trigger Inverter
7415	14	v13		K555L14		ЛМ4			Triple 3-Input AND Gate with Open Collector Outputs
7416	14	v13	K155LN5			ЛН5		FLH481T	Hex Inverter
7417	14	v14	K155LN4			ЛН4		FLH491T	Hex Buffer/Driver with 15V Open collector Outputs
7418	14	v13							Dual 4-input NAND Schmitt trigger
7419	14	v14							Hex Schmitt trigger Inverter
7420	14	v14	KR1531LA1	K555LA1	DL020D	ЛА1	FJH111	FLH121	Dual 4-Input NAND Gate
7421	14	v15		K555L16	DL021D	ЛМ6			Dual 4-Input AND Gate
7422	14	v13	K155LA7	K555LA7		ЛА7			Dual 4-Input NAND Gate with Open Collector Outputs
7423	16	v15	K155LE2			ЛЕ2		FLH511	Expandable Dual 4-Input NOR Gate with Strobe
7424	14	v16							Quad 2-Input NAND Gate
7425	14	v15	K155LE3			ЛЕ3		FLH521	Dual 4-Input NOR Gate with Strobe
7426	14	v15	K155LA11			ЛА11	FJH301	FLH291U	Quad 2-Input NAND Gate with 15V Open collector Outputs
7427	14	v15	K155LE4	K555LE4		ЛЕ4		FLH621	Triple 3-Input NOR Gate
7428	14	v15		K555LE5		ЛЕ5			Quad 2-Input NOR Gate Buffer
7430	14	v14	KR531LA2	K555LA2	DL030D	ЛА2	FJH101	FLH131	8-Input NAND Gate
7431	16	v13							Hex Delay Elements
7432	14	v14	K155L11	K555L11	DL032D	ЛЛ1		FLH631	Quad 2-Input OR Gate
7433	14	v15		K555LE11		ЛЕ11			Quad 2-Input NOR Gate Buffer with Open Collector Outputs
7434	14	v13		K555L19		ЛМ9			6x Buffer
7435	14	v13	K555IM7			ИМ7			6x Buffer, Open Collector
7436	14	v13							Quad 2-input NOR gate (different pinout than 7402)
7437	14	v13		K555LA12	DL037D	ЛА12		FLH531	Quad 2-Input Buffer
7438	14	v14		K555LA13	DL038D	ЛА13		FLH541	Quad 2-Input NAND Gate with Open Collector Outputs
7439	14	v13							Quad 2-Input NAND Gate
7440	14	v14		K555LA6	DL040D	ЛА6	FJH141	FLH141	Dual 4-Input NAND Gate
7441	16	v16							BCD to decimal decoder / Nixie tube driver
7442	16	v14		K555ID6		ИД6	FJH261	FLH281	Binary-Coded Decimal
7443	16	v16						FLH361	Excess-3 to Decimal Decoder
7444	16	v16						FLH371	Excess-3-Gray to Decimal Decoder
7445	16	v16						FLL111	BCD to decimal decoder/driver
7446	16	v15						FLL121U	BCD to 7-segment decoder/driver
7447	16	v15						FLL121V	BCD to 7-segment decoder/driver
7448	16	v15						FLH551	BCD to 7-segment decoder/driver
7449	14	v15							BCD to 7-segment decoder/driver
7450	14	v16	K131LR1			ЛР1	FJH151	FLH151	Dual 2-Wide 2-Input AND-OR-INVERT Gate
7451	14	v15					FJH161	FLH161	Dual 2-Wide 2-Input AND-OR-INVERT Gate (S1, H51, S51 only)
74LS51	14	v15		K555LR11	DL051D	ЛР11			Dual 2-2-3-3-Input AND-OR-INVERT Gate (LS1, LS51, ...)
7452	14	v18							Expandable 4-Wide 2-Input AND-OR Gate
7453	14	v15	K131LR3			ЛР3	FJH171	FLH171	Expandable 2-2-2-2-Input AND-OR-INVERT Gate (S3 only)
74H53	14	v13							Expandable 2-2-2-3-Input AND-OR-INVERT Gate (H53 only)
7454	14	v15					FJH181	FLH181	2-2-2-2-Input AND-OR-INVERT Gate (S4 only)
74H54	14	v15							2-2-2-3-Input AND-OR-INVERT Gate (H54 only)
74LS54	14	v14		K555LR13		ЛР13			2-2-3-3-Input AND-OR-INVERT Gate (LS4, LS54, ...)
74H55	14	v13							2-Wide 4-Input AND-OR-INVERT Gate (H55 only)
74LS55	14	v13							2-Wide 4-Input AND-OR-INVERT Gate (LS5, LS55, ...)
7456	8	TBD							50:1 frequency divider
7457	8	TBD							60:1 frequency divider
7458	14	v13							2-wide 2-input and 2-wide 3-input AND-OR gates
7460	14	v16	K155LD1			ЛД1			dual 4-input expander
7461	14	v13							Triple 3-Input Expander
7462	14	v17							3-2-2-3-Input Expander
7463	14	v13							Hex current sensing interface gates
7464	14	v16		K555LR9		ЛР9			4-3-2-2 input AND-OR-Invert gate
7465	14	v16	KR531LR10			ЛР10			4-2-3-2 Input AND-OR-INVERT Gate with Open collector Output
7468	16	v16							Dual 4-Bit Decade Counters (LS68, nicht L68)
7469	16	v16							Dual 4-Bit Binary Counters (LS69, nicht L69)
7470	14	v15					FJL101	FLJ101	AND gated J-K master-slave flip-flop, asynchronous preset and clear
74H71	14	v17							AND-OR gated J-K master-slave flip-flop, preset (H71 only)
74L71	14	v13							AND-OR-gated R-S master-slave flip-flop, preset and clear (L71 only)
7472	14	v15	K155TV1			TB1	FJL111	FLJ111	AND gated J-K master-slave flip-flop, asynchronous preset and clear
7473	14	v14					FJL121	FLJ121	Dual J-K Flip-Flop Flip-Flop with Clear
7474	14	v14	K131TM2	K555TM2	DL074D	ТМ2	FJL131	FLJ141	Dual D Positive Edge Triggered Flip-Flop with Preset and Clear
7475	16	v14		K555TM7		ТМ7	FJL181	FLJ151	4-bit Bistable Latch, complementary outputs

7476	16	v18				FJJ191	FLJ131	Dual J-K Flip-Flop with Preset and Clear	
7477	16	v14	SN74LS77		TM5			4-Bit Bistable Latch	
74H78	14	v13						Dual J-K Flip-Flop, Preset, Common Clock and Common Clear, positive clock (H78 only)	
74L78	14	v13						Dual J-K Flip-Flop, Preset, Common Clock and Common Clear, positive clock (L78 only)	
74LS78	14	v15						Dual J-K Flip-Flop, Preset, Common Clock and Common Clear, negative clock (LS78, ...)	
7479	14	v16						Dual D positive edge triggered flip-flop, asynchronous preset and clear	
7480	14	v16	K155IM1		IM1			Gated full adder	
7481	14	RAM	K155RU1		PY1			16-bit RAM (4 x 4)	
7482	14	v16	K155IM2		IM2			2-bit binary full adder	
7483	16	v15	K155IM3	K555IM3	IM3	FJH211	FLH241	4-Bit Binary Full Adder	
7484	16	RAM	K155RU3		PY3			16-bit RAM (4 x 4)	
7485	16	v14	KR531SP1	K555SP1	CP1		FLH431	4-Bit Magnitude Comparator	
74C85	16	v17						4-Bit Magnitude Comparator	
7486	14	v14	KR531LP5	K555LP5	DL086D	ЛП5	FJH271	FLH341	Quad 2-Input Exclusive-OR Gate
74L86	14	v18						Quad 2-Input Exclusive-OR Gate	
7487	14	v15						4-bit true/complement/zero/one element	
7488	16	ROM						256-bit ROM (32x8)	
7489	16	RAM						64-bit RAM (16x4), inverted outputs	
7490	14	v14 (!)		K555IE2	DL090D	IE2	FJJ141	FLJ161	4-Bit Decade Counter
74C90	14	v17						4-Bit Decade Counter	
7491	14	v17	K134IR2		IP2			8-Bit shift register, serial in, serial out, gated input	
7492	14	v15 (!)	K155IE4		IE4	FJJ251	FLJ171	Divide-by-12 Counter	
7493	14	v14 (!)		K555IE5	DL093D	IE5	FJJ211	FLJ181	4-Bit Binary Counter
74C93	14	v17						4-Bit Binary Counter	
7494	16	v17						4-Bit shift Register, dual asynchronous presets	
74C95	14	v18						4-Bit Shift Register	
74L95	14	v15						4-Bit Shift Register	
7495	14	v14		K555IR1		IP1	FJJ231	FLJ191	4-Bit Shift Register (7495 and 74LS95)
7496	16	v18						5-bit parallel-in/parallel-out shift register, asynchronous preset	
7497	16	v17	K155IE8		IE8			synchronous 6-bit binary rate multiplier	
7498	16	v15	K134IR5		IP5			4-bit data selector/storage register	
7499	16	TBD						4-bit bidirectional universal shift register	
74100	24	v18						Dual 4-bit bistable latch	
74101	14	v13						AND-OR-gated J-K negative-edge-triggered Flip-Flop, preset	
74102	14	v13						AND-gated J-K negative-edge-triggered Flip-Flop, preset and clear	
74103	14	v15						Dual J-K negative-edge-triggered Flip-Flop, clear	
74104	14	v15						J-K master-slave Flip-Flop	
74105	14	v15						J-K master-slave Flip-Flop, J2 and K2 inverted	
74106	16	v13						Dual J-K negative-edge-triggered Flip-Flop, preset and clear	
74107	14	v14		K555TV6		TB6	FJJ261	FLJ271	Dual J-K Flip-Flop with Clear
74108	14	v13						Dual J-K negative-edge-triggered Flip-Flop, preset, common clear and common clock	
74109	16	v15	K155TV15			TB15		Dual J-Not-K Positive-Edge-Triggered Flip-Flop with Clear and Preset	
74110	14	v15						AND-gated J-K master-slave flip-flop, data lockout	
74111	16	v16						Dual J-K Flip-Flop with Preset and Clear	
74112	16	v15		K555TV9	DL112D	TB9		Dual J-K Negative-Edge-Triggered Flip-Flop with Clear and Preset	
74113	14	v15	KR531TV10			TB10		Dual J-K Negative-Edge-Triggered Flip-Flop with Preset	
74114	14	v16	KR531TV11			TB11		Dual J-K Negative-Edge-Triggered Flip-Flop with Preset	
74115	14	v17						Dual J-K Flip-Flop Flip-Flop with Clear	
74116	24	v15						Dual 4-bit latch, clear	
74118	16	v15 (!)						Hex set/reset latch, common reset	
74119	24	v17 (!)						hex set/reset latch	
74120	14	v17						Dual pulse synchronizer/drivers	
74121	14	n/a						monostable multivibrator	
74122	16	n/a						retriggerable monostable multivibrator, clear	
74123	16	n/a		K533AG3	DL123D	AF3		dual retriggerable monostable multivibrator, clear	
74124	16	n/a	KR531GG1			ГГ1		dual voltage-controlled oscillator	
74125	14	v14	K155LP8	K555LP8		ЛП8		Quad Bus Buffer with Three-State Outputs	
74126	14	v14		K555LP14		ЛП14		Quad Bus Buffer with Three-State Outputs	
74128	14	v15	K155LE6			ЛЕ6		quad 2-input NOR gate	
74130	16	n/a						retriggerable monostable multivibrator	
74131	16	v16						quad 2-input AND gate	
74132	14	v14	KR531TL3		DL132D	ТЛ3	FLH601	Quad 2-Input NAND Gate	
74133	16	v13						13-Input NAND Gate	
74134	16	v13	KR531LA19			ЛА19		12-input NAND	
74135	16	v13						XOR/NOR gate	
74136	14	v15		K555LP12		ЛП12		Quad 2-Input Exclusive OR with Open Collector Outputs	
74137	16	v15						3 to 8-line Decoder/Demultiplexer with Address Latch	
74138	16	v14	KR531ID7	K555ID7		ИД7		3 to 8-line Decoder/Demultiplexer	
74139	16	v14	KR531ID14			ИД14		Dual 2 to 4-line Decoder/Demultiplexer	
74140	14	v13	KR531LA16			ЛА16		Dual 4-Input NAND Gate	
74141	16	v14	K155ID1			ИД1	FJL151	FLL101	BCD to decimal decoder/driver for cold-cathode indicator / Nixie tube
74142	16	TBD						decade counter/latch/decoder/driver for Nixie tubes	
74143	24	v18						decade counter/latch/decoder/7-segment driver	
74144	24	v18						decade counter/latch/decoder/7-segment driver	
74145	16	v14		K555ID10		ИД10	FLL111T	BCD to decimal decoder/driver	
74147	16	v14		K555IV3		ИВ3		10-Line to 4-Line Priority Encoder	
74148	16	v14		K555IV1		ИВ1		8-Line to 3-Line Priority Encoder	
74149	20	v13						8-line to 8-line priority encoder	
74150	24	v14		K555KP1		КП1	FLY111	16-line to 1-line data selector/multiplexer	
74151	16	v14	KR531KP7	K555KP7		КП7	FLY121	8-line to 1-Line Data Selector/Multiplexer	
74152	14	v13	K155KP5			КП5		8-line to 1-line data selector/multiplexer, inverting output	
74153	16	v14	KR531KP2	K555KP2		КП2	FLY131	Dual 4-Line to 1-Line Data Selector/Multiplexer	
74154	24	v14		K555ID3		ИД3	FJH341	FLY141	4-Line to 16-Line Decoder/Demultiplexer
74155	16	v14		K555ID4	DL155D	ИД4	FJH491	FLY151	Dual 2-Line to 4-Line Decoder/Demultiplexer
74156	16	v15		K555ID5		ИД5	FLY161	Dual 2-Line to 4-Line Decoder/Demultiplexer with Open Collector Outputs	
74157	16	v14		K555KP16		КП16	FLY171	Quad 2-Line to 1-Line Data Selector/Multiplexer	
74158	16	v15		K555KP18		КП18		Quad 2-Line to 1-Line Data Selector/Multiplexer	
74159	24	v15						4-Line to 16-Line Decoder/Demultiplexer, open collector	
74160	16	v15	K155IE9			IE9	FLJ401	Synchronous 4-Bit Decade Counter with Asynchronous Clear	
74161	16	v14	KR531IE10	K555IE10		IE10	FLJ411	Synchronous 4-Bit Binary Counter with Asynchronous Clear	
74162	16	v13		K555IE11		IE11	FLJ421	Synchronous 4-Bit Decade Counter with Synchronous Clear	
74163	16	v14		K555IE18		IE18	FLJ431	Synchronous 4-Bit Binary Counter with Synchronous Clear	
74164	14	v14		K555IR8	DL164D	ИР8	FLJ441	8-Bit Parallel-Out Serial Shift Register with Asynchronous Clear	
74165	16	v15		K555IR9		ИР9	FLJ451	8-Bit Serial Shift Register	
74166	16	v14		K555IR10		ИР10	FLJ461	8-Bit Shift register Register	
74167	16	n/a						synchronous decade rate multiplier	
74168	16	v16	KR531IE16			IE16		synchronous presettable 4-bit up/down decade counter	

74169	16	v16	KR531IE17			IE17			synchronous presettable 4-bit up/down binary counter
74170	16	RAM	K155SRP1	K555IR32		IP32	FLQ131		4 by 4 Register File with Open Collector Outputs
74171	16	v18							Quad D Flip-Flops with Clear
74172	24	v18							16-bit multiple port register file (8x2), comprehensive test using port 2 using SRAM testing
74173	16	v14	K155IR15	K555IR15		IP15			Quad D Flip-Flop with Three-State Outputs
74174	16	v14	KR531TM9	K555TM9		TM9	FLJ531		Hex D Flip-Flop with Common Clear
74175	16	v14	KR531TM8	K555TM8	DL175D	TM8	FLJ541		Quad D Edge-Triggered Flip-Flop with Complementary Outputs and Asynchronous Clear
74176	14	v16							presettable decade (bi-quinary) counter/latch
74177	14	v16							4-bit parallel-access shift register
74178	14	v17							4-bit parallel-access shift register, asynchronous clear input, complementary Qd output
74179	16	v17							4-bit parallel-access shift register, asynchronous clear input, complementary Qd output
74180	14	v14		K555IP2		IP2	FJH281	FLH421	9-bit odd/even parity bit generator and checker
74181	24	v13	KR531IP3	K555IP3		IP3	FJH451	FLH401	4-Bit Arithmetic Logic Unit and Function Generator
74182	16	v13	K155IP4			IP4		FLH411	Lookahead Carry Generator
74183	14	v15		K555IM5		IM5			Dual carry-save full adder
74184	16	v16	K155PR6			PP6		FLH561	BCD to binary converter
74185	16	v15	K155PR7			PP7		FLH571	6-bit binary to BCD converter (->check also with 7488 ROM)
74186	24	ROM							512-bit ROM (64x8)
74187	16	ROM							1024-bit ROM (256x4)
74188	16	ROM							256-bit PROM (32x8)
74189	16	RAM	KR531RU8			PY8			64-bit RAM (16x4), inverting outputs
74190	16	v14		K555IE12		IE12	FLJ201		Synchronous Up/Down Decade Counter
74191	16	v15		K555IE13		IE13	FLJ211		Synchronous Up/Down Binary Counter
74192	16	v14		K555IE6	DL192D	IE6	FLJ241		Synchronous Up/Down Decade Counter with Clear
74193	16	v15		K555IE7	DL193D	IE7	FJJ411	FLJ251	Synchronous Up/Down Binary Counter with Clear
74194	16	v14	KR531IR11	K555IR11A	DL194D	IP11	FLJ551		4-Bit Bidirectional Universal Shift Register
74195	16	v14	KR531IR12			IP12	FLJ561		4-Bit Parallel-Access Shift Register
74196	14	v15	KR531IE14	K555IE14		IE14	FLJ381		4-bit decade counter
74197	14	v16	KR531IE15	K555IE15		IE15			4-bit binary counter
74198	24	v18	K155IR13			IP13			8-bit bidirectional universal shift register
74199	24	v18							8-bit universal shift register, J-Not-K serial inputs
74200	16	RAM							256-bit RAM (256x1)
74201	16	RAM							256-bit RAM (256x1)
74207	16	RAM							1024-bit RAM (256x4)
74208	20	RAM							1024-bit RAM (256x4), separate data in- and outputs
74209	16	RAM							1024-bit RAM (1024x1)
74211	20	RAM							144-bit RAM (16x9) with output latch
74212	20	RAM							144-bit RAM (16x9)
74213	20	RAM							192-bit RAM (16x12)
74214	16	RAM							1024-bit RAM (1024x1)
74215	16	RAM							1024-bit RAM (1024x1) with power-down mode
74216	16	RAM							1024-bit RAM (1024x1) with power-down mode
74217	20	RAM							1024-bit RAM (1024x1) with power-down mode
74218	20	RAM							1024-bit RAM (1024x1) with power-down mode
74219	20	FIFO							64-bit RAM (16x4), non-inverting outputs
74221	16	n/a							Dual monostable multivibrator
74222	20	FIFO							64-bit FIFO memory (16x4), synchronous, input/output ready enable
74224	16	FIFO							64-bit FIFO memory (16x4), synchronous
74225	16	FIFO	KR531RU10			PY10			80-bit FIFO memory (16x5), asynchronous
74227	20	FIFO							64-bit FIFO memory (16x4), synchronous, input/output ready enable
74228	16	FIFO							64-bit FIFO memory (16x4), synchronous
74229	20	FIFO							80-bit FIFO memory (16x5), asynchronous
74232	16	FIFO							64-bit FIFO memory (16x4), asynchronous
74233	20	FIFO							80-bit FIFO memory (16x5), asynchronous
74234	16	FIFO							256-bit FIFO memory (64x4), asynchronous
74235	20	FIFO							320-bit FIFO memory (64x5), asynchronous
74236	16	FIFO							256-bit FIFO memory (64x4), asynchronous
74237	16	v13							3-of-8 Decoder/Demultiplexer with Address Latch
74238	16	v15							3-of-8 Decoder/Demultiplexer
74240	20	v15	KR531AP3	K555AP3		AP3			octal buffer, inverting outputs
74241	20	v15	KR531AP4	K555AP4		AP4			octal buffer, non-inverting outputs
74242	14	v13		K555IP6		IP6			Quad Bus Transceiver with Inverted Three-State Outputs
74243	14	v15		K555IP7		IP7			Quad Bus Transceiver with Noninverted Three-State Outputs
74244	20	v14		K555AP5		AP5			Octal Buffer with Noninverted Three-State Outputs
74245	20	v14		K555AP6		AP6			Octal Bus Transceiver with Noninverted Three-State Outputs
74246	16	v15							BCD to 7-segment decoder/driver
74247	16	v15		K555ID18		ID18			BCD to 7-segment decoder/driver
74248	16	v15							BCD to 7-segment decoder/driver
74249	16	v15							BCD to 7-segment decoder/driver
74251	16	v14	KR531KP15	K555KP15	DL251D	KP15			8-line to 1-line Data Selector/Multiplexer with Three-State Outputs
74253	16	v15	KR531KP12	K555KP12	DL253D	KP12			Dual 4-line to 1-line Data Selector/Multiplexer with Three-State Outputs
74256	16	v18							dual 4-bit addressable latch
74257	16	v14	KR531KP11	K555KP11	DL257D	KP11			Quad 2-line to 1-line Data Selector/Multiplexer with Noninverted Three-Outputs
74258	16	v15	KR531KP14	K555KP14		KP14			Quad 2-line to 1-line Data Selector/Multiplexer with Inverted Three-State Outputs
74259	16	v14		K555IR30	DL259D	IP30			8-Bit Addressable Latch
74260	14	v15	KR531LE7			LE7			Dual 5-Input NOR Gate
74261	16	v17		K555IP8		IP8			2-bit by 4-bit parallel binary multiplier
74262	20	ROM							5760-bit ROM (Teletext character set, 128 characters 5x9)
74265	16	v16							quad complementary output elements
74266	14	v13		K555LP13		LP13			Quad 2-Input Exclusive NOR Gate with Open Collector Outputs
74269	24	v18							8-bit bidirectional binary counter
74270	16	ROM							2048-bit ROM (512x4)
74271	20	ROM							2048-bit ROM (256x8)
74273	20	v15		K555IR35		IP35			8-bit register, asynchronous clear
74276	20	v18							Quad J-Not-K edge-triggered Flip-Flops, separate clocks, common preset and clear
74278	14	v13							4-bit cascadeable priority registers, latched data inputs
74279	16	v16		K555TR2		TP2			quad set-reset latch
74280	14	v14	KR531IP5	K555IP5		IP5			9-Bit Odd/Even Parity Generator/Checker
74281	24	TBD							4-bit parallel binary accumulator
74283	16	v15		K555IM6		IM6			4-Bit Binary Full Adder
74284	16	v16							4-bit by 4-bit parallel binary multiplier (low order 4 bits of product)
74285	16	v16							4-bit by 4-bit parallel binary multiplier (high order 4 bits of product)
74286	14	v17							9-bit parity generator/checker, bus driver parity I/O port
74287	16	ROM							1024-bit PROM (256x4)
74288	16	ROM							256-bit PROM (32x8)
74289	16	RAM	KR531RU9			PY9			64-bit RAM (16x4), inverted outputs
74290	14	v16	K555IE20			IE20			decade counter (separate divide-by-2 and divide-by-5 sections)

74292	16	v16					Programmable Frequency Divider/Digital Timer
74293	14	v13					4-Bit Binary Counter
74294	16	v13					Programmable Frequency Divider/Digital Timer
74295	14	v16		K555IR16	DL295D	IP16	4-bit bidirectional shift register
74297	16	n/a					digital phase-locked loop filter
74298	16	v13		K555KP13		KП13	Quad 2-Input Multiplexer with Storage
74299	20	v14	KR531IR24		DI299D	IP24	8-Bit Bidirectional Universal Shift/Storage Register with Three-State Outputs
74300	16	RAM					256-bit RAM (256x1)
74301	16	RAM					256-bit RAM (256x1)
74309	16	RAM					1024-bit RAM (1024x1)
74311	20	RAM					144-bit RAM (16x9) with output latch
74312	20	RAM					144-bit RAM (16x9)
74313	20	RAM					192-bit RAM (16x12)
74314	16	RAM					1024-bit RAM (1024x1)
74315	16	RAM					1024-bit RAM (1024x1) with power-down mode
74316	16	RAM					1024-bit RAM (1024x1) with power-down mode
74317	20	RAM					1024-bit RAM (1024x1) with power-down mode
74318	20	RAM					1024-bit RAM (1024x1) with power-down mode
74320	16	n/a					crystal-controlled oscillator
74321	16	n/a					crystal-controlled oscillators, F/2 and F/4 count-down outputs
74322	20	v17					8-Bit Shift Register, Sign Extend
74323	20	v16	KR531IR29			IP29	8-bit Bidirectional Universal Shift/Storage Register, Synchronous Clear
74347	16	v16					BCD-to-7 segment decoders/drivers, low voltage version of 7447
74348	16	v13		K555IV2		ИВ2	8 to 3-line priority encoder
74350	16	v18		KP1531IR42		IP42	4-bit shifter
74351	20	v18					dual 8-line to 1-line data selectors/multiplexers, 4 common data inputs
74352	16	v16		K555KP19		KП19	dual 4-line to 1-line data selectors/multiplexers, inverting outputs
74353	16	v16		K555KP17		KП17	dual 4-line to 1-line data selectors/multiplexers, inverting outputs
74354	20	v13					8-line to 1-line data selector/multiplexer, transparent registers
74355	20	v13					8-line to 1-line data selector/multiplexer, transparent registers
74356	20	v13					8-line to 1-line data selector/multiplexer, edge-triggered registers
74361	22	n/a					bubble memory function timing generator
74363	20	v13					octal transparent latch
74364	20	v13					octal edge-triggered D-type register
74365	16	v15	K155LP10			ЛП10	Hex Buffer with Noninverted Three-State Outputs
74366	16	v15	K155LN6			ЛН6	Hex Buffer with Inverted Three-State Outputs
74367	16	v14	K155LP11			ЛП11	Hex Buffer with Noninverted Three-State Outputs
74368	16	v15		K555LN7		ЛН7	Hex Buffer with Inverted Three-State Outputs
74370	16	ROM					2048-bit ROM (512x4)
74371	20	ROM					2048-bit ROM (256x8)
74373	20	v14	KR531IR22	K555IR22		IP22	Octal Register with Three-State Outputs
74374	20	v14	KR531IR23	K555IR23	DL374D	IP23	Octal Register with Three-State Outputs
74375	16	v15		K555TM10		ТМ10	Quad Bistable Latch
74376	16	v16					quad J-Not-K flip-flop, common clock and common clear
74377	20	v14		K555IR27		IP27	8-Bit Register with Clock Enable
74378	16	v16					6-Bit Register with Clock Enable
74379	16	v16					4-bit register, clock enable and complementary outputs
74381	20	v18	KR531IK2			ИК2	4-bit arithmetic logic unit/function generator, generate and propagate outputs
74382	20	v18					4-bit arithmetic logic unit/function generator, ripple carry and overflow outputs
74384	16	n/a		K555IP9		IP9	8-bit by 1-bit two's complement multipliers
74385	20	v18					Quad serial adder/subtractor
74386	14	v13					Quad 2-Input Exclusive-OR Gate
74387	16	ROM					1024-bit PROM (256x4)
74388	16	v13					4-bit D-type register
74390	16	v15					Dual 4-Bit Decade Counter
74393	14	v15		K555IE19		ИЕ19	Dual 4-Bit Binary Counter
74395	16	v16					4-bit cascadable shift register
74396	16	v16		K555IR43		IP43	Octal storage registers, parallel access
74398	20	v16					Quad 2-input multiplexers, storage and complementary outputs
74399	16	v15		K555KP20		KП20	Quad 2-input multiplexer, storage
745400	18	RAM					4096-bit SRAM (4k x 1)
745401	18	RAM					4096-bit SRAM (4k x 1)
745405	16	v18					1 out of 8 binary decoder (equivalent to i8205)
745408	48	n/a					64K Dynamic RAM Controller
745409	48	n/a					256K Dynamic RAM Controller
74412	24	v17	K589IR12		DS8212D	IP13	Multi-mode buffered 8-bit latches (equivalent to Intel 3212/8212)
74413	16	FIFO					256-bit FIFO memory (64x4)
74422	14	n/a					retriggerable monostable multivibrators, two inputs
74423	14	n/a					dual retriggerable monostable multivibrator
74425	14	v16					Quad Bus Buffer with Three-State Outputs
74426	14	v16					Quad Bus Buffer with Three-State Outputs
74429	28	n/a					FIFO RAM controller
74432	24	v18					Multi-mode buffered 8-bit latches, inverted outputs
74436	16	v18					Line driver/memory driver circuits - MOS memory interface, damping output resistor
74437	16	v18					Line driver/memory driver circuits - MOS memory interface
74440	20	v17					quad tridirectional bus transceiver, non-inverting outputs
74441	20	v17					quad tridirectional bus transceiver, inverting outputs
74442	20	v17					quad tridirectional bus transceiver, non-inverting outputs
74443	20	v17					quad tridirectional bus transceiver, inverting outputs
74444	20	v17					quad tridirectional bus transceiver, inverting and non-inverting outputs
74445	16	v13					BCD-to-decimal decoders/drivers
74446	16	v18					Quad bus transceivers, direction controls, inverting outputs
74447	16	v16					BCD to 7-segment decoder/driver
74448	20	v17					quad tridirectional bus transceiver, inverting and non-inverting outputs
74449	16	v18					Quad bus transceivers, direction controls, non-inverting outputs
745450	16	ROM					8192-bit PROM (1024x8) with power-down
745451	16	ROM					8192-bit PROM (1024x8) with power-down
74456	16	v18					4-bit NBCD full adder
74461	24	v17					8-Bit Presettable Binary Counter
74462	20	n/a					fiber-optic data-link transmitter
74463	20	n/a					fiber-optic data-link receiver
74465	20	v17	KR1533AP14	K555AP14		АП14	Octal buffer, non-inverting outputs
74466	20	v13	KR1533AP15	K555AP15		АП15	Octal buffers, inverting outputs
74467	20	v13					Octal buffers, non-inverting outputs
74468	20	v13					Octal buffers, inverting outputs
74470	16	ROM					2048-bit PROM (256x8)
74471	20	ROM					2048-bit PROM (256x8)

74472	20	ROM			4096-bit PROM (512x8)
74473	20	ROM			4096-bit PROM (512x8)
74474	24	ROM			4096-bit PROM (512x8)
74475	24	ROM			4096-bit PROM (512x8)
74476	18	ROM			4096-bit PROM (1024x4)
74477	18	ROM			4096-bit PROM (1024x4)
74478	16	ROM			8192-bit PROM (1024x8) with power-down
74479	16	ROM			8192-bit PROM (1024x8) with power-down
74490	16	v13			Dual decade counter
74518	20	v13			8-bit comparator, open collector
74519	20	v13			8-bit comparator, open collector
74520	20	v13			8-bit comparator, inverting output
74521	20	v15	KP1531SP2	CP2	8-bit comparator, inverting output
74522	20	v13			8-bit comparator, inverting output, open collector
74526	20	v18			fuse programmable identity comparator, 16-bit (assumes that all fuses are unset)
74527	20	v18			fuse programmable identity comparator, 8-bit + 4-bit conventional Identity comparator (assumes that all fuses are unset)
74528	16	v18			fuse programmable Identity comparator, 12-bit (assumes that all fuses are unset)
74533	20	v15	K555IR40	IP40	Octal transparent latch, inverting outputs
74534	20	v15	KP1531IR41	IP41	Octal register, inverting outputs
74537	20	v18			1 of 10 decoder / BCD to decimal decoder
74538	20	v18			1 of 8 decoder with 3-state outputs
74539	20	v18			dual 2-line to 4-line decoder/demultiplexer
74540	20	v15		DLS40D	Octal buffers and line drivers
74541	20	v15		DLS41D	Octal buffers and line drivers
74543	24	v17			Octal Registered Transceiver, non-inverting
74544	24	v17			Octal Registered Transceiver, inverting
74545	20	v18			Octal bidirectional transceiver, non-inverting
74546	24	TBD			8-bit bidirectional registered transceiver, non-inverting
74547	24	TBD			8-bit bidirectional latched transceiver, non-inverting
74548	20	v18			3 to 8 decoder/multiplexer
74557	40	n/a			8-bit by 8-bit multiplier
74558	40	n/a			8-bit by 8-bit multiplier
74560	20	v18			4-bit decade counter
74561	20	v18			4-bit binary counter
74563	20	v15			8-bit D-type transparent latch, inverting outputs
74564	20	v15			8-bit D-type edge-triggered register, inverting outputs
74566	24	TBD			8-bit bidirectional registered transceiver, inverting
74567	24	TBD			8-bit bidirectional latched transceiver, inverting
74568	20	v17			4-bit Decade Up/Down Counter
74569	20	v17			4-bit Binary Up/Down Counter
74570	16	ROM			2048-bit PROM (512x4)
74571	20	ROM			2048-bit PROM (512x4)
74572	18	ROM			4096-bit PROM (1024x4)
74573	18	ROM			4096-bit PROM (1024x4)
74L573	20	v15	K555IR33	IP33	Octal D Latch with Tri-State Outputs (all, except 745273)
74574	20	v15	K555IR37	IP37	Octal D-type edge-triggered flip-flop
74575	20	v17			Octal D-type edge-triggered flip-flop, synchronous clear
74576	20	v16			Octal D-type edge-triggered flip-flop, inverting outputs
74577	24	v18			Octal D-type edge-triggered flip-flop, synchronous clear, inverting outputs
74579	20	v18			8-bit bidirectional binary counter
74580	20	v16			Octal D-type transparent latch, inverting outputs
74582	24	TBD			4-bit BCD arithmetic logic unit
74583	16	v18			4-bit BCD adder
74588	20	v18			Octal bidirectional transceiver, non-inverting
74589	16	v13			8-Bit Shift Register with Input Latch with Three-State Outputs
74590	16	v16			8-bit binary counter, output registers
74591	16	v18			8-bit binary counter, output registers
74592	16	v18			8-bit binary counter, input registers
74593	16	v18			8-bit binary counter, input registers
74594	16	v18			8-bit shift registers, Serial-In, Parallel-Out, output latches
74595	16	v15	K555IR52	IP52	8-bit shift registers, Serial-In, Parallel-Out, output latches, output enable
74596	16	v16			8-bit shift registers, Serial-In, Parallel-Out, output latches, output enable
74597	16	v13			Serial-out Shift Register with Input Latches
74598	20	TBD			8-bit shift register, Selectable Parallel-In/Out input latches
74599	16	v18			8-bit shift registers, Serial-In, Parallel-Out, output latches
74604	28	v18			Octal 2-input multiplexer, latch, high-speed, Three-State
74605	28	v18			Octal 2-input multiplexer, latch, high-speed, Open Collector
74606	28	v18			Octal 2-input multiplexer, latch, glitch-free, Three-State
74607	28	v18			Octal 2-input multiplexer, latch, glitch-free, Open Collector
74612	40	n/a			PC/AT Memory Mapper
74620	20	v13	K555AP26	AP26	Octal bus transceiver, inverting, Three-State Outputs
74621	20	v13			Octal bus transceiver, non-inverting, open collector
74622	20	v13			Octal bus transceiver, inverting
74623	20	v13			Octal bus transceiver, non-inverting, Three-State Outputs
74624	14	n/a			voltage-controlled oscillator, enable control, range control, two-phase outputs
74625	16	n/a			dual voltage-controlled oscillator, two-phase outputs
74626	16	n/a			dual voltage-controlled oscillator, enable control, two-phase outputs
74627	14	n/a			dual voltage-controlled oscillator
74628	14	n/a			voltage-controlled oscillator, enable control, range control,
74629	16	n/a			dual voltage-controlled oscillator, enable control, range control
74638	20	v15			Octal bus transceiver, inverting outputs
74639	20	v15			Octal bus transceiver, non-inverting outputs
74640	20	v15	K555AP9	AP9	Octal bus transceiver, inverting outputs
74641	20	v15			Octal bus transceiver, non-inverting outputs
74642	20	v15			Octal bus transceiver, inverting outputs
74643	20	v15	K555AP16	AP16	Octal bus transceiver, mix of inverting and non-inverting outputs
74644	20	v15			Octal bus transceiver, mix of inverting and non-inverting outputs
74645	20	v13			Octal bus transceiver, non-inverting outputs
74646	24	v17	K555AP10	AP10	Octal bus transceiver/latch/multiplexer, non-inverting outputs, Three-State
74647	24	v18			Octal bus transceiver/latch/multiplexer, non-inverting outputs, Open-Collector
74648	24	v17			Octal bus transceiver/latch/multiplexer, inverting outputs, Three-State
74649	24	v18			Octal bus transceiver/latch/multiplexer, inverting outputs, Open-Collector
74651	24	v17	K555AP17	AP17	Octal bus transceiver/register, inverting outputs
74652	24	v17	K555AP24	AP24	Octal bus transceiver/register, non-inverting outputs
74653	24	v17			Octal bus transceiver/register, inverting outputs
74654	24	v17			Octal bus transceiver/register, non-inverting outputs
74657	24	TBD			octal bidirectional transceiver with 8-bit parity generator/checker

74666	24	v17			8-bit D-type transparent read-back latch, non-inverting
74667	24	v17			8-bit D-type transparent read-back latch, inverting
74668	16	v17			Synchronous 4-bit decade up/down counter
74669	16	v17			Synchronous 4-bit binary up/down counter
74670	16	RAM	K555IR26	IP26	4 by 4 Register File with Three-State Outputs
74671	20	TBD			4-bit bidirectional shift register/latch/multiplexer, direct clear
74672	20	TBD			4-bit bidirectional shift register/latch/multiplexer, synchronous clear
74673	24	v17			16-bit serial-in, serial/parallel-out shift register, output storage registers
74674	24	TBD			16-bit parallel-in, serial-out shift register
74679	20	v18			12-bit address comparator, enable
74680	20	v18			12-bit address comparator, latch
74682	20	v16			8-bit magnitude comparator, P>Q output
74683	20	v16			8-bit magnitude comparator, P>Q output, open collector
74684	20	v16			8-bit magnitude comparator, P>Q output
74685	20	v16			8-bit magnitude comparator, P>Q output, open collector
74686	24	v18			8-bit magnitude comparator, P>Q output, enable
74687	24	v18			8-bit magnitude comparator, P>Q output, enable
74688	20	v15			8-bit magnitude comparator, enable
74689	20	v13			8-bit magnitude comparator, enable, open collector
74690	20	v18			4-bit decimal counter/latch/multiplexer, asynchronous clear
74691	20	v18			4-bit binary counter/latch/multiplexer, asynchronous clear
74692	20	v18			4-bit decimal counter/latch/multiplexer, synchronous clear
74693	20	v18			4-bit binary counter/latch/multiplexer, synchronous clear
74696	20	TBD			4-bit decimal up/down counter/register/multiplexer, asynchronous clear
74697	20	TBD			4-bit binary up/down counter/register/multiplexer, asynchronous clear
74698	20	TBD			4-bit decimal up/down counter/register/multiplexer, synchronous clear
74699	20	TBD			4-bit binary up/down counter/register/multiplexer, synchronous clear
74740	20	v15			Octal Buffer, inverting outputs
74741	20	v13			Octal Buffer, non-inverting outputs
74744	20	v15			Octal Buffer with non-inverted Three-State Outputs
74748	16	v14			8 to 3-line priority encoder (glitch-less)
74756	20	v15			Octal Buffer, inverting outputs, Open Collector and Schmidt trigger
74757	20	v13			Octal Buffer, non-inverting outputs, Open Collector and Schmidt trigger
74758	20	v18			Quad bus transceivers, inverting outputs, open-collector
74759	20	v18			Quad bus transceivers, non-inverting outputs, open-collector
74760	20	v15			Octal buffer/line driver, non-inverting outputs
74762	20	v18			Octal buffer/line driver, inverting and non-inverting outputs
74763	20	v18			Octal buffer/line driver, inverting outputs, complementary enable inputs
74779	16	TBD			8-bit bidirectional binary counter
74783	40	n/a			synchronous address multiplexer for display systems (= MC6883)
74795	20	v16			Octal buffer, non-inverting, common enable
74796	20	v16			Octal buffer, inverting, common enable
74797	20	v16			Octal buffer, non-inverting, enable for 4 buffers each
74798	20	v16			Octal buffer, inverting, enable for 4 buffers each
74804	20	v15			Hex 2-input NAND drivers
74805	20	v15			Hex 2-input NOR drivers
74808	20	v17			Hex 2-input AND drivers
74810	14	v17			Quad 2-input XNOR gates
74821	24	v13			10-bit bus interface flip-flop
74823	24	v13			9-bit D-type flip-flops, clear and clock enable inputs
74824	24	v13			9-bit D-type flip-flops, clear and clock enable inputs, inverting inputs
74825	24	v13			8-bit D-type flip-flop, clear and clock enable inputs
74832	20	v17			Hex 2-input OR drivers
74843	24	v18			9-bit D flip-flops, clear and set inputs
74844	24	v18			9-bit D flip-flops, clear and set inputs, inverting inputs
74848	16	v18			8 to 3-line priority encoder (glitch-less)
74857	24	v13			Hex 2-line to 1-line multiplexer
74867	24	v18			synchronous 8-bit up/down counter, asynchronous clear
74869	24	v18			synchronous 8-bit up/down counter, synchronous clear
74873	24	v18	KR531IR34	IP34	Dual 4-bit transparent latch with clear
74874	24	v18	KR531IR38	IP38	Dual 4-bit edge-triggered D flip-flops with clear
74876	24	v18			Dual 4-bit edge-triggered D flip-flops with clear, inverting outputs
74878	24	v18			Dual 4-bit D-type flip-flop, synchronous clear, non-inverting outputs
74879	24	v18			Dual 4-bit D-type flip-flop, synchronous clear, inverting outputs
74880	24	v18			Dual 4-bit transparent latch with clear, inverting outputs
74885	24	v18			8-bit magnitude comparator
74ALS900	14	v17			Quad 2-Input NAND Gate
74C901	14	v13			Hex inverting CMOS to TTL buffer
74ALS902	14	v17			Quad 2-Input NOR Gate
74C902	14	v13			Hex non-inverting CMOS to TTL buffer
74ALS903	14	v17			Quad 2-Input NAND Gate with Open Collector Outputs
74C903	14	v18			Hex inverting TTL to CMOS buffer
74C904	14	v18			Hex non-inverting TTL to CMOS buffer
74C906	14	v13			Hex inverting NMOS buffer
74C907	14	n/a			Hex inverting PMOS buffer
74C910	18	RAM			RAM 64 x 4 bit
74C914	14	v18			Hex Schmitt trigger Inverter
74C915	18	v18			7-segment to BCD
74C920	22	RAM			RAM 256 x 4 bit
74C921	18	RAM			RAM 256 x 4 bit
74C929	16	RAM			RAM 1024 x 1 bit
74C930	16	RAM			RAM 1024 x 1 bit
74S940	20	v18			octal buffer, inverting outputs
74S941	20	v18			octal buffer, non-inverting outputs
74C989	16	RAM			RAM 16 x 4 bit
74990	20	v18			8-bit D-type transparent read-back latch, non-inverting
74992	24	v18			9-bit D-type transparent read-back latch, non-inverting
74994	24	v18			10-bit D-type transparent read-back latch, non-inverting
74996	24	TBD			8-bit D-type edge-triggered read-back latch
741000	14	v18	KR1553LA21	ЛA21	use 7400
741002	14	v18	KR1553LE1	ЛE10	use 7402
741003	14	v18	KR1553LA23	ЛA23	use 7403
741004	14	v18	KR1553LH8	ЛH8	use 7404
741005	14	v18	KR1553LH10	ЛH10	use 7405
741008	14	v18	KR1553LI8	ЛI8	use 7408
741010	14	v18	KR1553LA24	ЛA24	use 7410
741011	14	v18	KR1553LI10	ЛI10	use 7411

741020	14	v18	KR1553LA22	ЛА22	use 7420
741032	14	v18	KR1553LL4	ЛЛ4	use 7432
741034	14	v18	KR1553LP16	ЛП16	use 7434
741035	14	v18	KR1553LP17	ЛП17	use 7435
741240	20	v18			use 74240
741241	20	v18			use 74241
741242	14	v18			use 74242
741243	14	v18			use 74243
741244	20	v18			use 74244
741245	20	v18			use 74245
741620	20	v18			use 74620
741621	20	v18			use 74621
741622	20	v18			use 74622
741623	20	v18			use 74623
741638	20	v18			use 74638
741639	20	v18			use 74639
741640	20	v18			use 74640
741641	20	v18			use 74641
741642	20	v18			use 74642
741643	20	v18			use 74643
741644	20	v18			use 74644
741645	20	v18			use 74645
742708	14	ROM			8192-bit PROM (1024x8)
743037	16	v18			Quad 2-input NAND
743708	14	ROM			8192-bit PROM (1024x8)
747001	14	v16			Quad 2-input AND gate
747002	14	v16			Quad 2-input NOR gate
747014	14	v16			Hex non-inverting buffer
747032	14	v16			Quad 2-input OR gates
747266	14	v17			Quad 2-input XNOR gate
747403	16	FIFO			256-bit FIFO memory (64x4)
747404	16	FIFO			320-bit FIFO memory (64x5)
748541	20	v18			8-bit buffer, selectable inverting/non-inverting
749034	20	v17			Nine-wide buffer, inverting
749035	20	v17			Nine-wide buffer
749114	20	v17			Nine-wide buffer, inverting
749115	20	v17			Nine-wide buffer
749134	20	v17			Nine-wide buffer, inverting
749135	20	v17			Nine-wide buffer
749240	24	v17			9-bit buffer / line driver, inverting
749244	24	v17			9-bit buffer / line driver, non-inverting
749245	24	v17			9-bit bidirectional transceiver, non-inverting
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4000	14	v14	K176LP4	ЛП4	Dual 3-input NOR gate + 1 NOT gate
4001	14	v14	K176LE5	ЛЕ5	Quad 2-input NOR gate
4002	14	v14	K176LE6	ЛЕ6	Dual 4-input NOR gate
4006	14	TBD	K176IR10	ИР10	18-stage shift register (four independent with common clock: two 4-stage, two 5-stage with Q4 tap)
4007	14	v17	K176LP1	ЛП1	Dual complementary transistor pair + 1 NOT gate (pin 5 and 8 untested)
4008	16	v14	K176IM1	ИМ1	4-bit binary full adder
4009	16	v15	K176PU2	ПУ2	Hex inverter gate, dual power supply, can drive 1 TTL/DTL load (replaced by 4049)
4010	16	v15	K176PU3	ПУ4	Hex buffer gate, dual power supply, can drive 1 TTL/DTL load (replaced by 4050)
4011	14	v14	K176LA7	ЛА7	Quad 2-input NAND gate
4012	14	v14	K176LA8	ЛА8	Dual 4-input NAND gate
4013	14	v15	K176TM2	ТМ2	Dual D-type flip-flop
4014	16	v15			8-stage parallel in shift register (synchronous parallel load, serial in, Q6/Q7/Q8 out) (see 4021)
4015	16	v14	K176IR2	ИР2	Dual 4-stage shift register (two independent: serial in, Q1/Q2/Q3/Q4 out, reset, clock)
4016	14	v14	K176KT1	КТ1	Quad bilateral switch
4017	16	v14	K176IE8	ИЕ8	Decade counter with 10 decoded outputs (5-stage Johnson counter)
4018	16	v16	K561IE19	ИЕ19	Presetable divide-by-N counter
4019	16	v14	K561LS2	ЛС2	Quad AND/OR select gate
4020	16	v14	K561IE16	ИЕ16	14-stage binary ripple counter
4021	16	v15			8-stage parallel in shift register (synchronous parallel load, serial in, Q6/Q7/Q8 out) (see 4021)
4022	16	v14	K561IE9	ИЕ9	Octal counter with 8 decoded outputs (4-stage Johnson counter)
4023	14	v14	K176LA9	ЛА9	Triple 3-input NAND gate
4024	14	v14	K176IE1	ИЕ1	7-stage binary ripple counter
4025	14	v15	K176LE10	ЛЕ10	Triple 3-input NOR gate
4026	16	v15	K176IE4	ИЕ4	Decade counter with decoded 7-segment display outputs and display enable
4027	16	v14	K176TV1	ТВ1	Dual J-K master-slave flip-flop
4028	16	v14	K176ID1	ИД1	BCD to decimal (1-of-10) decoder active HIGH output
4029	16	v14	K561IE14	ИЕ14	Presetable up/down counter, binary or BCD-decade
4030	14	v15	K176LP2	ЛП2	Quad XOR gate (replaced by 4070)
4031	16	TBD	K176IR4	ИР4	64-stage shift register
4032	16	v16			Triple serial adder
4033	16	v15	K176IE5	ИЕ5	Decade counter with decoded 7-segment display outputs and ripple blanking
4034	24	v17	K561IR6	ИР6	8-stage bidirectional parallel/serial input/output register
4035	16	v16	K561IR9	ИР9	4-stage parallel-in/parallel-out shift register
4036	24	RAM			RAM 4 x 8 bit
4038	16	v16			Triple serial adder
4039	24	RAM			RAM 4 x 8 bit
4040	16	v14	KR1561IE20	ИЕ20	12-stage binary ripple counter
4041	14	v14			Quad buffer/inverter (two outputs for each input) (4 times standard "B" drive)
4042	16	v14	K561TM3	ТМ3	Quad D-type latch
4043	16	v14	K561TR2	ТР2	Quad NOR R-S latch with tri-state outputs
4044	16	v15			Quad NAND R-S latch with tri-state outputs
4045	16	n/a			21-stage counter
4046	16	n/a	KR1561GG1	ГГ1	Phase-locked loop with VCO
4047	14	n/a			Monostable/astable multivibrator
4048	16	v15			Multifunctional expandable 8-input gate with tri-state output
4049	16	v15	K561LH2	ЛН2	Hex inverter gate, can drive 2 TTL/RTL loads or 4 four 74LS loads
4050	16	v14	K561PU4	ПУ4	Hex buffer gate, can drive 2 TTL/RTL loads or 4 four 74LS loads
4051	16	v15	K561KP2	КП2	8-channel analog multiplexer/demultiplexer
4052	16	v15	K561KP1	КП1	Dual 4-channel analog multiplexer/demultiplexer
4053	16	v14	KR1561KP5	КП5	Triple 2-channel analog multiplexer/demultiplexer
4054	16	v15			BCD to 7-segment decoder/LCD driver
4055	16	v15			BCD to 7-segment decoder/LCD driver with "display-frequency" output
4056	16	v15			BCD to 7-segment decoder/LCD driver with strobed-latch function
4059	24	v18	K561IE15	ИЕ15	Programmable divide-by-N counter
4060	16	v16			14-stage binary ripple counter and oscillator, schmitt trigger inputs

4061	16	RAM	K176RU2	PY2	RAM 256 x 1 bit
4063	16	v16			4-bit digital comparator
4066	14	v14	K561KT3	KT3	Quad analog switch (low "ON" resistance)
4067	24	v16			16-channel analog multiplexer/demultiplexer (1-of-16 switch)
4068	14	v15			8-input NAND/AND gate (2 outputs)
4069	14	v14	564PU7	ПY7	Hex inverter
4070	14	v14	KR1561LP14	ЛП14	Quad 2-input XOR gate
4071	14	v14			Quad 2-input OR gate
4072	14	v14			Dual 4-input OR gate
4073	14	v14			Triple 3-input AND gate
4075	14	v14			Triple 3-input OR gate
4076	16	v14	KR1561IR14	ИP14	Quad D-type register with tri-state outputs
4077	14	v14			Quad 2-input XNOR gate
4078	14	v14			8-input NOR/OR gate (2 outputs)
4081	14	v14	K561LI2	ЛИ2	Quad 2-input AND gate
4082	14	v14			Dual 4-input AND gate
4085	14	v15			Dual 2-wide, 2-input AND/OR invert (AOI)
4086	14	v15			Expandable 4-wide, 2-input AND/OR invert (AOI)
4089	16	n/a			Binary rate multiplier
4093	14	v14	K561TL1	ТЛ1	Quad 2-input NAND gate, schmitt trigger inputs
4094	16	v14	KR1561PR1	ПP1	8-stage shift-and-store bus
4095	16	v16			Gated J-K flip-flop (non-inverting)
4096	16	v16			Gated J-K flip-flop (inverting and non-inverting)
4097	24	v14			Differential 8-channel analog multiplexer/demultiplexer
4098	16	n/a	KR1561AG1	АГ1	Dual one-shot monostable
4099	16	v14			8-bit addressable latch
4106	14	v18			Hex inverter gate, schmitt trigger inputs
40014	14	v18			Hex Schmitt trigger Inverter
40097	16	v18			Hex Buffer with Noninverted Three-State Outputs
40098	16	v18			Hex Buffer with Inverted Three-State Outputs
40100	16	n/a			32-stage left/right shift register
40101	16	v13	1526IP6	ИP6	9-bit parity generator/checker
40102	16	v17			Presettable 2-decade BCD down counter
40103	16	v17			Presettable 8-bit binary down counter
40104	16	v16			4-bit bidirectional universal shift register with output enable
40105	16	FIFO			4-bit x 16 word FIFO register
40106	14	v14			Hex inverter gate, schmitt trigger inputs
40107	8	v16	KR1561LA10	ЛА10	Dual 2-input NAND gate with 136mA open-drain driver (32 times standard "B" sink)
40109	16	v16	1526PU6	ПY6	Quad level shifter
40110	16	v16			Up/down decade counter, latch, 7-segment decoder, LED driver
40116	22	v17	564PU9	ПY9	8-bit bidirectional CMOS-to-TTL level converter (checks logic only, not levels)
40117	14	n/a			Programmable dual 4-bit terminator
40147	16	v13			10-line to 4-line (BCD) priority encoder
40160	16	v13			Decade counter/asynchronous clear
40161	16	v14	K561IE21	ИE21	Binary counter/asynchronous clear
40162	16	v14			Decade counter/synchronous clear
40163	16	v16			Binary counter/synchronous clear
40174	16	v14			Hex D-type flip-flop
40175	16	v15			Quad D-type flip-flop
40192	16	v16			Presettable 4-bit up/down BCD counter
40193	16	v16			Presettable 4-bit up/down binary counter
40194	16	v16			4-bit bidirectional universal shift register with reset
40240	20	v16			Buffer/Line driver; inverting (tri-state)
40244	20	v16			Buffer/line driver; non-inverting (tri-state)
40245	20	v16			Octal bus transceiver; (tri-state) outputs
40257	16	v16			Quad 2-line to 1-line data selector/multiplexer (tri-state)
40373	20	v16			Octal D-type transparent latch (tri-state)
40374	20	v16			Octal D-type flip-flop; positive-edge trigger (tri-state)
4501	16	v16			Triple Gate
4502	16	v15	KP561LN1	ЛH1	Hex inverting buffer (tri-state)
4503	16	v13	K561LH3	ЛH3	Hex non-inverting buffer with tri-state outputs
4504	16	v13			Hex voltage level shifter for TTL-to-CMOS or CMOS-to-CMOS operation
4505	14	RAM			RAM 64 x 1 bit
4506	16	v16			2x 2-2 AND-OR-INVERT
4507	14	v16			Quad 2-Input Exclusive-OR Gate
4508	24	v15			Dual 4-bit latch with tri-state outputs
4510	16	v15			Presettable 4-bit BCD up/down counter
4511	16	v14	K1564ID23	ИД23	BCD to 7-segment latch/decoder/driver
4512	16	v14	KP1561KP3	КП3	8-input multiplexer (data selector) with tri-state output
4513	18	v16			BCD to 7-segment latch/decoder/driver
4514	24	v13			1-of-16 decoder/demultiplexer active HIGH output
4515	24	v13			1-of-16 decoder/demultiplexer active LOW output
4516	16	v16	KR1561IE11	ИE11	Presettable 4-bit binary up/down counter
4517	16	n/a			Dual 64-stage shift register
4518	16	v13			Dual BCD up counter
4519	16	v14	K561KP4	КП4	Quad 2-input multiplexer (data selector)
4520	16	v14	K561IE10	ИE10	Dual 4-bit binary up counter
4521	16	n/a			24-stage frequency divider
4522	16	v16			Programmable BCD divide-by-N counter
4526	16	v17			Programmable 4-bit binary down counter
4527	16	n/a			BCD rate multiplier
4528	16	n/a			Dual retriggerable monostable multivibrator with reset
4529	16	v13			Dual 4-channel analog data selector/multiplexer
4530	16	v16			Dual 5-input majority logical gate
4531	16	v15			13-input parity checker/generator
4532	16	v15			8-bit priority encoder
4534	24	n/a			Cascaded BCD Counters
4536	16	n/a			Programmable Timer
4538	16	n/a			Dual retriggerable precision monostable multivibrator
4539	16	v16			Dual 4-input multiplexer
4541	14	TBD			Programmable Timer
4543	16	v15			BCD to 7-segment latch/decoder/driver with phase input
4549	16	n/a			Successive approximation registers
4551	16	n/a			Quad 2-channel analog multiplexer/demultiplexer
4553	16	n/a			3-digit BCD counter
4555	16	v14	K561ID6	ИД6	Dual 1-of-4 decoder/demultiplexer active HIGH output
4556	16	v14	K561ID7	ИД7	Dual 1-of-4 decoder/demultiplexer active LOW output

4557	16	TBD			1-to-64 stage variable length shift register
4558	16	v16			BCD to 7-segment decoder (enable, RBI and provides active-high output)
4559	16	n/a			Successive approximation registers
4560	16	v16			NBCD adder
4561	14	v16			9's complementer
4566	16	n/a			Industrial time-base generator
4568	16	n/a			Phase Comparator and Programmable Counters
4569	16	TBD			Programmable divide-By-N, dual 4-Bit binary/BCD down counter
4572	16	v15			Hex gate: quad NOT, single NAND, single NOR
4574	16	v18 (*)			Quad Comparator
4583	16	n/a			Dual adjustable schmitt trigger inputs, each with buffer and inverter outputs, and XOR output
4584	14	v14			Hex inverter gate, schmitt trigger inputs
4585	16	v16	K5611P2	ИП2	4-bit digital comparator
4598	18	v17			8-bit addressable latch
4316	16	v17			4x Analog Switch/Multiplexer/Demultiplexer (only digitally tested)
4720	16	RAM			RAM 256 x 1 bit
4723	16	v18			4-bit addressable latch
4724	16	v16			8-bit addressable latch
4929	16	v15			2x NAND, 4x Inverter
4930	14	v13			4x 2-input NAND
4931	14	v13			2x 5-input NAND
4934	14	v18			6x Inverter with open collector outputs
4935	14	v18			6x Inverter
49700	16	v15			2x NAND Gate, 2x AND Gate with 15V Open collector Outputs
49701	16	v15			4x Drivers with open collector outputs
49702	16	v15			4-bit D Register with Clear
49703	16	v13			6x delay gates
49704	16	v13			2x Binary Counter
49705	16	v15			2x Decimal Counter
49713	14	v13			Dual 3-input NAND Schmitt trigger
49714	8	v13			2:4 Decoder
7303	20	v14			octal bus transceiver, inverting outputs
7304	20	v14			octal bus transceiver, non-inverting outputs
75121	16	v18			Dual line driver
75122	16	v18			Triple line driver (N8T14)
75123	16	v18			Dual line driver
75125	16	v18			7x line receiver, inverting outputs
75127	16	v18			7x line receiver, inverting outputs
75154	16	v18			4x line receiver
75160	20	v16			octal bus transceiver
75172	16	v18			4x line driver (SN65173)
75173	16	v18			4x line driver
75189	14	v15			Quadruple line drivers (MC1489)
75450	14	v17			2x AND high power
75451	8	v14	K155LA5	ЛА5	2x AND high power
75452	8	v14	K155LA18	ЛА18	2x NAND high power
75453	8	v14	K155LL2	ЛЛ18	2x OR high power
75454	8	v14			2x NOR high power
75460	14	v17	K1102AP10	АП10	2x AND high power
75461	8	v17	K1102AP11	D461D АП11	2x AND high power
75462	8	v17	K1102AP12	АП12	2x NAND high power
75463	8	v17	K1102AP13	АП13	2x OR high power
75464	8	v17	K1102AP14	АП14	2x NOR high power
75466	16	v18			7x darlington arrays
75467	16	v18			7x darlington arrays
75468	16	v17			7x darlington arrays
75469	16	v18			7x darlington arrays
75470	14	v18			2x AND high power
75471	8	v18			2x AND high power
75472	8	v18			2x NAND high power
75473	8	v18			2x OR high power
75474	8	v18			2x NOR high power
75494	16	v13	KR1010KT1	КТ1	Hex digit driver
75497	16	v18			MOS to LED 7-channel driver
75498	20	v18			MOS to LED 9-channel driver
7707	20	v15			MOS7707, Hex Inverter with Open Collector = 74LS06
7708	20	v15			MOS7708, Quad 2-line to 1-line Data Selector/Multiplexer with Noninverted TS-Outputs = 74LS257
7709	20	v13			MOS7709, Quad 2-line to 1-line Data Selector/Multiplexer with Inverted TS-Outputs = 74LS258
7711	20	v15			MOS7711, Dual 2 to 4-line Decoder/Demultiplexer = 74LS139
7712	20	v15			MOS7712, Quad 2-input AND = 74LS08
7713	20	v15			MOS7713, Hex Inverter = 74LS04
7714	20	v15			MOS7714, Quad 2-input NOR = 74LS02
7715	20	v15			MOS7715, Octal Register with Three-State Outputs = 74LS173
80C95	16	v13			Hex Buffer with Noninverted Three-State Outputs
80C96	16	v13			Hex Buffer with Inverted Three-State Outputs
80C97	16	v13			Hex Buffer with Noninverted Three-State Outputs
80C98	16	v13			Hex Buffer with Inverted Three-State Outputs
81LS95	20	v17			Octal buffer, non-inverting, common enable
81LS96	20	v17			Octal buffer, inverting, common enable
81LS97	20	v17			Octal buffer, non-inverting, enable for 4 buffers each
81LS98	20	v17			Octal buffer, inverting, enable for 4 buffers each
82C19	24	v17			16-line to 1-line data selector/multiplexer
8205	16	v18			1 out of 8 binary decoder (equivalent to SN74S405)
8212	24	v17			Multi-mode buffered 8-bit latches (equivalent to Intel 3212/8212)
8216	16	v18			Quad parallel bidirectional bus driver (equivalent to Intel 3216/8216/M5L8216)
8226	16	v18			Quad parallel bidirectional bus driver, inverting outputs (equivalent to Intel 3226/8226/M5L8226)
8259	28	v13	KR580VN59	BH59	programmable interrupt controller
8303	20	v14			octal bus transceiver, inverting outputs = 7303
8304	20	v14			octal bus transceiver, non-inverting outputs = 7304
8708	20	v15			MOS8708, Quad 2-line to 1-line Data Selector/Multiplexer with Noninverted TS-Outputs = 74LS257
8713	20	v15			MOS8713, Hex Inverter = 74LS04
88C29	14	v15			Quad Single-Ended Line Driver
88C30	14	v15			Dual Differential Line Driver
8T26	16	v17			Quad Bus Driver/Receiver Inverting Outputs
8T28	16	v17			Quad Bus Driver/Receiver
8T95	16	v17			Hex Buffer with Noninverted Three-State Outputs
MC6885	16	v17			Hex Buffer with Noninverted Three-State Outputs
8T96	16	v17			Hex Buffer with Inverted Three-State Outputs

MC6886	16	v17			Hex Buffer with Inverted Three-State Outputs
8T97	16	v17			Hex Buffer with Noninverted Three-State Outputs
MC6887	16	v17			Hex Buffer with Noninverted Three-State Outputs
8T98	16	v17			Hex Buffer with Inverted Three-State Outputs
MC6888	16	v17			Hex Buffer with Inverted Three-State Outputs
DM9002	14	v18			Quad 2-Input NAND Gate
DM9003	14	v18			Triple 3-Input NAND Gate
DM9004	14	v18			Dual 4-Input NAND Gate
DM9012	14	v18			Quad 2-Input NAND Gate with Open Collector Outputs
DM9016	14	v18			Hex Inverter
DM9024	16	v18			Dual J-Not-K Positive-Edge-Triggered Flip-Flop with Clear and Preset
DM9368	16	v18			BCD to 7-segment decoder/driver
DM9370	16	v18			BCD to 7-segment decoder/driver with Open Collector Outputs
DS3630	14	v18			Hex TTL buffer (=DS1630)
DS3631	8	v18			2x AND high power
DS3632	8	v18			2x NAND high power
DS3633	8	v18			2x OR high power
DS3634	8	v18			2x NOR high power
DS8640	14	v18			Quad NOR Unified Driver (=7640)
DS8641	16	v18			Quad NOR Unified Driver (=7641)
DS8810	14	v18			Quad 2-Input NAND Gate with Open Collector Outputs (=7810)
DS8811	14	v18			Quad 2-Input NAND Gate with Open Collector Outputs (=7811)
DS8812	14	v18			Hex Inverter (=7812)
DS8819	14	v18			Quad 2-Input AND Gate with Open collector Outputs (=7819)
CA3045	14	v18			5x NPN arrays
CA3046	14	v18			5x NPN arrays
CA3081	16	v17			7x NPN arrays
CA3082	16	v17			7x NPN arrays
CA3083	16	v18			5x NPN arrays
CA3086	14	v18			5x NPN arrays
CA3161	16	v18			BCD to 7-segment decoder/driver
L20x	16	v18			4x darlington arrays
L70x	16	v18			7x darlington arrays
TD6208x	18	v18			8x darlington arrays
TD6278x	18	v18			8x darlington arrays
ULN200x	16	v14			7x darlington arrays
ULN202x	16	v18			7x darlington arrays
ULN2064	16	v18			4x darlington arrays (also ULN2066)
ULN2074	16	v18	K1109KT3	KT3	4x darlington arrays (also ULN2076)
ULN280x	18	v14			8x darlington arrays
ULN282x	18	v17			8x darlington arrays
UDN6118	18	v17			8x VFD driver
UDN298x	18	v18			8x darlington arrays
V40511	16	v18			BCD to 7-segment decoder/driver
LMx39	14	v18 (*)			Quad Differential Comparators LM139, LM239, LM339 (Analog Device)
uA741	8	v18 (*)			General-Purpose Operational Amplifiers (Analog Device)
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DIS1417	14	v18			Hex Display (DIS1417)
DL1414	8	v18			4-Character Display (DL1414)
DL2416	18	v18			4-Character Display (DL2416)
HP730x	8	v18			Dec Display (HP5082-7300 and HP5082-7302)
HP7340	8	v18			Hex Display (HP5082-7340)
TIL306/307	14	v18			Dec Display (TIL306/TIL307)
TIL308/309	14	v18			Dec Display (TIL308/TIL309)
TIL311	14	v18			Hex Display (TIL311)
1x7 Seg.	10	v17			1x 7 Segment LED with DP, CC: 3, 8
1x7 Seg.	10	v17			1x 7 Segment LED with DP, CA: 3, 8
1x7 Seg.	10	v17			1x 7 Segment LED with DP, CC: 1, 6
1x7 Seg.	10	v17			1x 7 Segment LED with DP, CA: 1, 6
1x7 Seg.	10	v17			1x 7 Segment LED with DP, CC: 7, 9
1x7 Seg.	10	v17			1x 7 Segment LED with DP, CA: 7, 9
2x7 Seg.	10	v17			2x 7 Segment LED with DP, CC: 5, 10
2x7 Seg.	10	v17			2x 7 Segment LED with DP, CA: 5, 10
2x7 Seg.	10	v17			2x 7 Segment LED with DP, CC: 7, 8
2x7 Seg.	10	v17			2x 7 Segment LED with DP, CA: 7, 8
2x7 Seg.	18	v17			2x 7 Segment LED with DP, CC: 13, 14
2x7 Seg.	18	v17			2x 7 Segment LED with DP, CA: 13, 14
3x7 Seg.	12	v17			3x 7 Segment LED with DP, CC: 8, 9, 12
3x7 Seg.	12	v17			3x 7 Segment LED with DP, CA: 8, 9, 12
4x7 Seg.	12	v17			4x 7 Segment LED with DP, CC: 6, 8, 9, 12
4x7 Seg.	12	v17			4x 7 Segment LED with DP, CA: 6, 8, 9, 12
8x8 Dot Matrix	16	v17			8x8 Dot Matrix, CC: 13, 3, 4, 10, 6, 11...
8x8 Dot Matrix	16	v17			8x8 Dot Matrix, CA: 13, 3, 4, 10, 6, 11...